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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/995,624	11/29/2001	Tadashi Fujimura	111235	2285
25944	7590	02/08/2006	EXAMINER	
OLIFF & BERRIDGE, PLC P.O. BOX 19928 ALEXANDRIA, VA 22320			AGGARWAL, YOGESH K	
			ART UNIT	PAPER NUMBER
			2615	

DATE MAILED: 02/08/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

<p align="center"><b>Advisory Action</b> <b>Before the Filing of an Appeal Brief</b></p>	<b>Application No.</b> 09/995,624	<b>Applicant(s)</b> FUJIMURA, TADASHI	
	<b>Examiner</b> Yogesh K. Aggarwal	<b>Art Unit</b> 2615	

**--The MAILING DATE of this communication appears on the cover sheet with the correspondence address --**

THE REPLY FILED 13 January 2006 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE.

1. ☒ The reply was filed after a final rejection, but prior to or on the same day as filing a Notice of Appeal. To avoid abandonment of this application, applicant must timely file one of the following replies: (1) an amendment, affidavit, or other evidence, which places the application in condition for allowance; (2) a Notice of Appeal (with appeal fee) in compliance with 37 CFR 41.31; or (3) a Request for Continued Examination (RCE) in compliance with 37 CFR 1.114. The reply must be filed within one of the following time periods:
- a) ☒ The period for reply expires 3 months from the mailing date of the final rejection.
- b) ☐ The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection.

Examiner Note: If box 1 is checked, check either box (a) or (b). ONLY CHECK BOX (b) WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).

Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**NOTICE OF APPEAL**

2. ☐ The Notice of Appeal was filed on \_\_\_\_\_. A brief in compliance with 37 CFR 41.37 must be filed within two months of the date of filing the Notice of Appeal (37 CFR 41.37(a)), or any extension thereof (37 CFR 41.37(e)), to avoid dismissal of the appeal. Since a Notice of Appeal has been filed, any reply must be filed within the time period set forth in 37 CFR 41.37(a).

**AMENDMENTS**

3. ☐ The proposed amendment(s) filed after a final rejection, but prior to the date of filing a brief, will not be entered because
- (a) ☐ They raise new issues that would require further consideration and/or search (see NOTE below);
- (b) ☐ They raise the issue of new matter (see NOTE below);
- (c) ☐ They are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or
- (d) ☐ They present additional claims without canceling a corresponding number of finally rejected claims.

NOTE: \_\_\_\_\_. (See 37 CFR 1.116 and 41.33(a)).

4. ☐ The amendments are not in compliance with 37 CFR 1.121. See attached Notice of Non-Compliant Amendment (PTOL-324).
5. ☐ Applicant's reply has overcome the following rejection(s): \_\_\_\_\_.
6. ☐ Newly proposed or amended claim(s) \_\_\_\_\_ would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s).
7. ☒ For purposes of appeal, the proposed amendment(s): a) ☐ will not be entered, or b) ☒ will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended.
- The status of the claim(s) is (or will be) as follows:
- Claim(s) allowed: \_\_\_\_\_.
- Claim(s) objected to: \_\_\_\_\_.
- Claim(s) rejected: 1,2 and 4-27.
- Claim(s) withdrawn from consideration: \_\_\_\_\_.

**AFFIDAVIT OR OTHER EVIDENCE**

8. ☐ The affidavit or other evidence filed after a final action, but before or on the date of filing a Notice of Appeal will not be entered because applicant failed to provide a showing of good and sufficient reasons why the affidavit or other evidence is necessary and was not earlier presented. See 37 CFR 1.116(e).
9. ☐ The affidavit or other evidence filed after the date of filing a Notice of Appeal, but prior to the date of filing a brief, will not be entered because the affidavit or other evidence failed to overcome all rejections under appeal and/or appellant fails to provide a showing of good and sufficient reasons why it is necessary and was not earlier presented. See 37 CFR 41.33(d)(1).
10. ☐ The affidavit or other evidence is entered. An explanation of the status of the claims after entry is below or attached.

**REQUEST FOR RECONSIDERATION/OTHER**

11. ☒ The request for reconsideration has been considered but does NOT place the application in condition for allowance because:  
See attached sheet.
12. ☐ Note the attached Information Disclosure Statement(s). (PTO/SB/08 or PTO-1449) Paper No(s). \_\_\_\_\_.
13. ☐ Other: \_\_\_\_\_.



**DAVID OMETZ**  
**SUPERVISORY PATENT EXAMINER**

**Examiner's response:**

1. Applicant argues with regards to claim 1 that Yamagata does not teach or suggest that basic image data present in image data memory 35 is deleted without deleting the simplified image data when basic image data is transferred from the image data memory 35 to the IC memory card M. The Examiner respectfully disagrees. Yamagata teaches an image pickup device (figure 2, CCD 28) that outputs an image signal output by the CCD 28 is processed by a signal processing circuit 33, and is then converted to a digital image signal by means of an analog-digital (A/D) converter 34, stored in a image data memory 35 as digital image data. Yamagata also teaches that a predetermined processing is applied to the image data stored in the image data memory 35 when the image data is transferred to the IC memory card M by an image data processing circuit 36 through an IC Memory card control circuit 41 (col. 4 lines 44-56). Yamagata further teaches in figures 10 and 11 a video card data compression mode wherein at step S163, a selected file (basic image data) that has been already transmitted therein from the image data memory 35 is read out of IC memory card M, is stored in the image data memory 35 after being compressed at step S165 and stored as compressed image data (simplified image data) (col. 11 lines 39-65, figure 11). Then at step S166, **the selected image file (basic image data) is deleted and stored in the IC memory card M, since the newly compressed image data will be stored. Finally at step S167, the newly compressed file stored in the image memory 35 is transferred to the IC memory card M to be stored therein (col. 11 line 65-col. 12 line 3).** Therefore in essence, a basic image data that was transmitted from the image data memory 35 to the IC card M is selected, compressed and then stored in the image data memory 35. The selected image data is then deleted from the IC card M and the compressed image data

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from the image data memory is stored in the IC card M and therefore meets the claimed limitations "and causes said internal storage device to store said basic image data and simplified image data and to delete said basic image data without deleting said simplified image data after said transmission device has transmitted said basic image data".

***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Yogesh K. Aggarwal whose telephone number is (571) 272-7360. The examiner can normally be reached on M-F 9:00AM-5:30PM.

2. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David Ometz can be reached on (571)-272-7593. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

3. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

YKA  
February 2, 2006



**DAVID OMETZ**  
SUPERVISORY PATENT EXAMINER